Project assignment #4: Circuit layout

Assigned 2/2/05    due 2/9/05

a. Layout your circuit using Clewin. Start with the pad layout provided under tools/Clewin. Use a minimum feature size of 8 µm and a minimum overlap of 4 µm. Start by laying out the circuit using non-overlapping rectangles. Don’t use wires, polygons etc. Merge diffused regions as needed rather than connecting them through via holes and metal wires. Bring your design to the lab period (USB memory stick/floppy or email it to yourself so you can access it through web email).