Figure 1. Processor and Cache System.

Figure 2. Direct Mapped Cache.
Depending on the which tag causes a hit, one of these blocks is selected, and then one of the bytes in the block is selected.

Figure 3. Fully Associative Cache.

Depending on the which tag causes a hit, one of these blocks is selected, and then one of the bytes in the block is selected.

Figure 4. 4-way Set Associative Cache.