Magic layout of a circuit in 1.2μm CMOS technology is shown below. The grid is 1 lambda by 1 lambda. The device parameters are the same as in Problem A.15.

a) Sketch the circuit and indicate the device sizes \( W/L \) in μm/μm.

b) If the DC current flowing into the node \( X \) is \( I_X = 10\mu A \), find the DC voltage \( V_X \), given that node \( GND \) is at 0V.
Legend:

ndiff = (n+)diffusion
ppc = p-substrate to metal1 contact
ndc = (n+)diffusion to metal1 contact
pc = poly to metal1 contact