For the BiCMOS circuits shown in Figs. a, b, c, and d of Problem A.23, find expressions and numerical values for the incremental resistances $R_i$ from each labeled voltage node $V_i$ to signal ground. Show all work and justify any approximations that you make.

The device parameters are as in Problem A.23, except that $\lambda_n = \lambda_p = 0.01 \frac{1}{V}$, and $V_A = 100$ V. You can use the DC solution from Problem A.23, i.e., you can assume that the DC solution is not affected significantly by the channel length modulation and Early effects.

To get credit, attach a copy of the pages with the circuit diagrams, and label your final answers (expressions and values for $R_i$ next to the corresponding voltage nodes).