QUESTION #4 - 35 pts.
Concepts: Pulse Ckt./Monostable/Integrator

\[ V_A \quad V_B \quad V_C \quad V_D \quad V_E \]

Note: Non-ideal diode behavior

\[ V_{A(t)} \quad V_{B(t)} \quad V_{C(t)} \quad V_{D(t)} \]

1.1 msec
QUESTION #4 - 35 pts

(A) 10 pts.

\[ V_A(t) \quad V_B(t) \]

Given: \( \frac{10k\Omega}{---} \), \( 0.25\mu F \), \( \text{Silicon Diode} \)

Place these components properly in Block I above.

(B) 10 pts.

Choose \( R_7 \) and \( C \) so the pulse width of \( V_D(t) \) is 1.1 msec.

\[ R_7 = \quad C = \]
QUESTION #4 - 35 PTS.

15 pts.

Block IV

$V_{D}(t)$

$V_{E}(t)$

$V_{Cap}(t)$

$R_{B}$

$C = 1 \mu F$

$V_{a}(0) = 0$ volts

$R_{B} = 16.5 \Omega$

Graph and label $V_{E}(t)$

$15 V$

$V_{D}(t)$

$\Phi V$

1.1 msec

$V_{E}(t)$

$t$