

PULSE WIDTH MODULATOR : USING A VOLTAGE INPUT TO CONTROL THE DUTY CYCLE OF THE OUTPUT WAVEFORM.

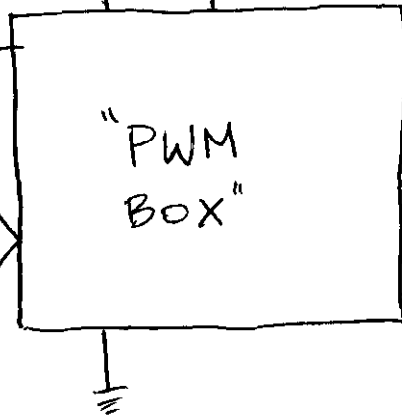
SPECS:

INPUTS

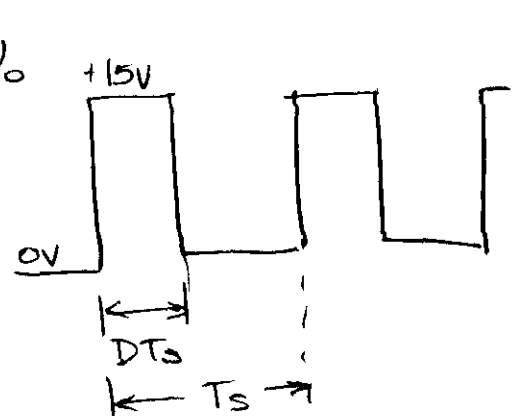
(ANALOG) V_m +
- ϕ

(DIGITAL) 8 BIT

+Vcc -Vcc
+15V -15V



OUTPUT



$$f_s = \frac{1}{T_s} = 50 \text{ KHz} \pm 2\%$$

D $\hat{=}$ DUTY CYCLE $0 \leq D \leq 1$

WITH ANALOG INPUT V_m : $D = \frac{V_m}{4V}$; $0 \leq V_m \leq 4V$

WITH DIGITAL INPUT :

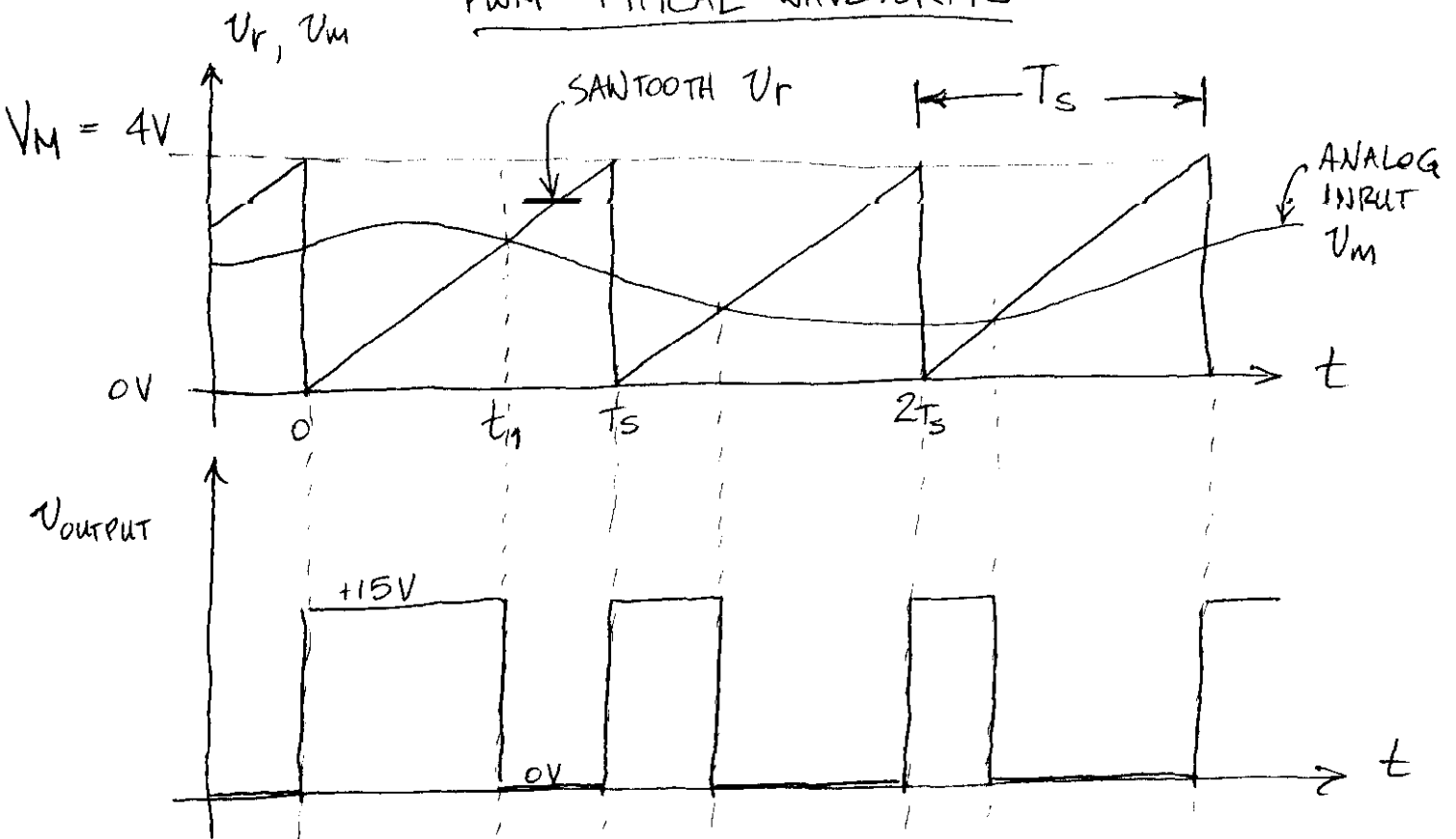
$$D = \frac{1}{256} \sum_{k=1}^8 A_k 2^{8-k}$$

8-BIT RESOLUTION
 $\left\{ A_1, A_2, \dots, A_8 \right\}$
 MSB .. LSB

EXAMPLE : IF $A_1 = 1$, $A_{i \neq 1} = 0$, THEN

$$D = \frac{1}{256} (2^7) = \frac{128}{256} = 0.5$$

PWM TYPICAL WAVEFORMS



IT IS DESIRED TO HAVE

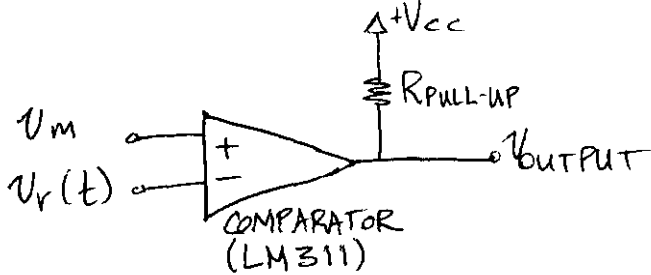
$$V_r(t) = \frac{t}{T_s} \cdot V_M ; 0 \leq t \leq T_s$$

AT TIME t_1 : $V_r(t_1) = V_M$

$$\frac{t_1}{T_s} \cdot V_M = V_M$$

$$\frac{t_1}{T_s} = \boxed{\frac{V_M}{V_M} = D}$$

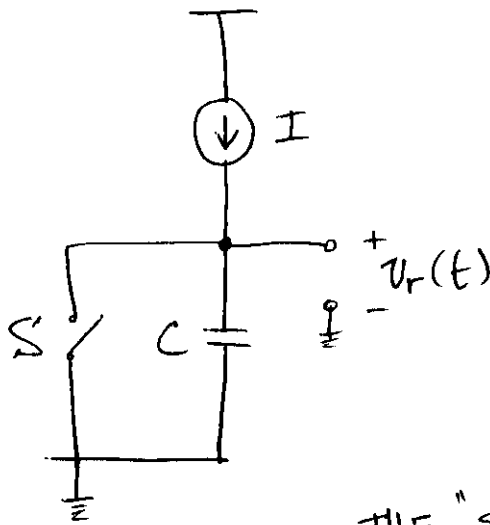
THIS IS A SPEC.



2 PRACTICAL LIMITS OF $V_r(t)$:

- A PERFECTLY LINEAR "RAMP" RISE UP TO V_M
 - & AN INSTANTANEOUS FALL BACK TO ZERO.
- (THESE ARE NOT ACHIEVABLE IN REAL LIFE, SO MAKE THEM AS CLOSE TO IDEAL AS YOU CAN)

A BASIC IDEA FOR THE SAWTOOTH GENERATOR $v_r(t)$



$$i_c(t) = C \frac{dv_c(t)}{dt}$$

$$v_c(t) = \frac{1}{C} \int i_c(t) dt$$

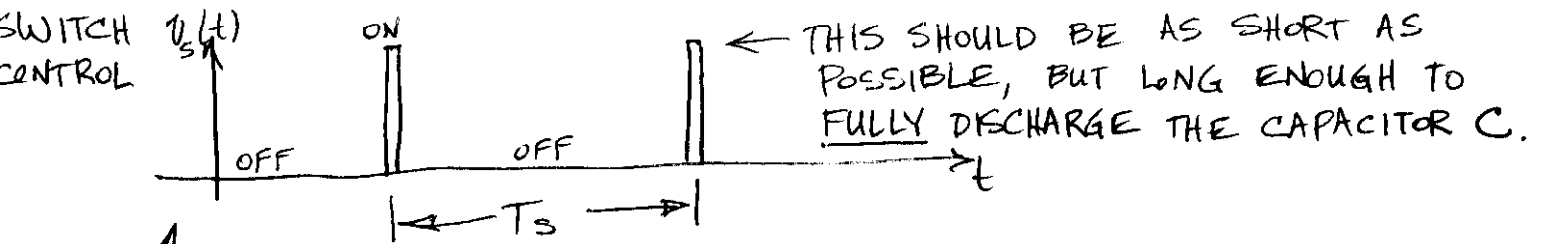
IF $i_c(t)$ IS A CONSTANT,

$v_c(t)$ BECOMES A RAMP.

THE "SWITCH" S SHOULD CLOSE ONCE

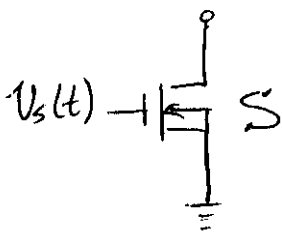
$v_c(t) = V_M$ TO RETURN $v_r(t)$ BACK TO ZERO.

HOW DO WE CONTROL THE SWITCH?

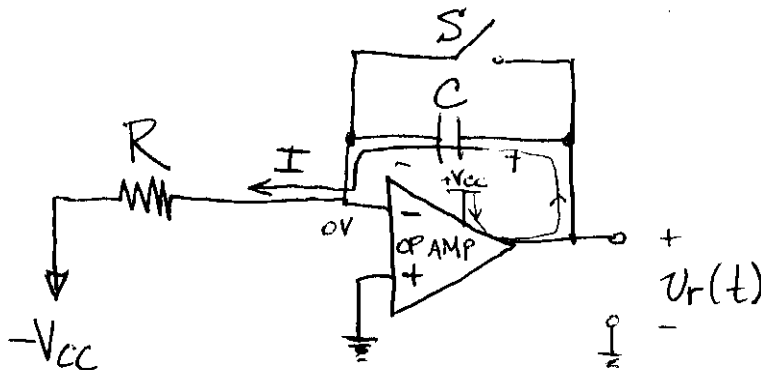


** THIS WAVEFORM CAN BE PRODUCED USING KNOWLEDGE FROM LAB #1.

← HERE'S ONE IMPLEMENTATION FOR THE SWITCH



TO PRODUCE I, ~~XXXXXXXXXX~~



$$I \approx \frac{0V - (-V_{CC})}{R}$$