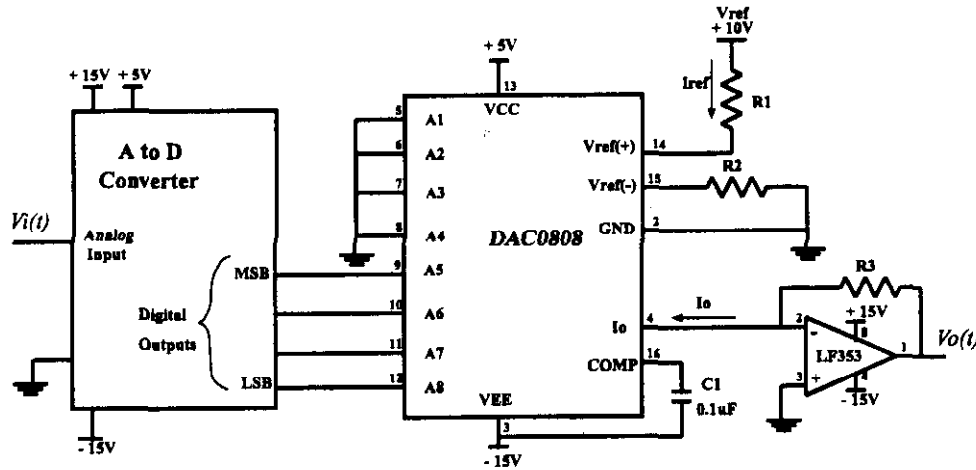


ECEN 4618
Advanced Electronics Lab
Spring 1999
Quiz # 6

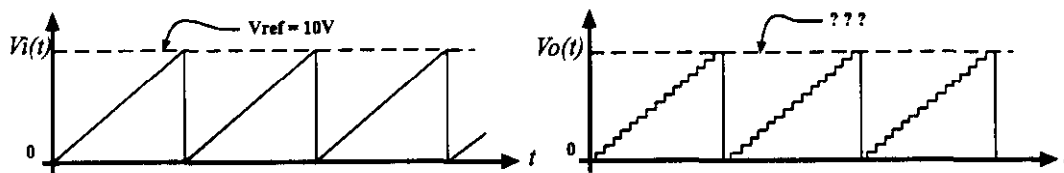
Name: SOLUTION



In order to test the operation of your group's 4-bit A/D converter, your lab partner assembles the (D/A) test circuit shown above. The test circuit output voltage V_o is given by the following expression:

$$V_o = V_{ref} \left(\frac{R_3}{R_1} \right) \left(\frac{1}{2^n} \sum_{k=1}^n A_k 2^{(n-k)} \right)$$

where $n = 8$ for an 8-bit D/A chip (like the DAC0808), k is the index of the input to the D/A chip, and $A_k = \{0,1\}$ depending on the digital state {low, high} of the k^{th} input bit. An input signal $V_i(t)$ is applied to the input of your A/D converter, and the output of the test circuit $V_o(t)$ is displayed on an oscilloscope. Plots of these two waveforms are shown below:



(5 points) If $R_1 = R_2 = R_3 = 1k\Omega$, what is the amplitude (maximum value) of $V_o(t)$?
(Hint: It is not the same as the amplitude of $V_i(t)$!)

PLUGGING INTO THE EQUATION ABOVE GIVES

$$|V_o| \approx 0.586 V \quad \text{FOR A MAXIMUM DIGITAL OUTPUT (1111)}$$

(5 points) Briefly explain why the two amplitudes are different.

THE A/D OUTPUTS ARE CONNECTED TO THE D/A'S 4 LEAST SIGNIFICANT BITS, INSTEAD OF THE 4 MOST SIGNIFICANT BITS.

(5 points) What should the value of R_3 be changed to so that the amplitude of $V_o(t)$ will be the same as the amplitude of $V_i(t)$?

CHANGE R_3 TO BE $\approx 17 k\Omega$