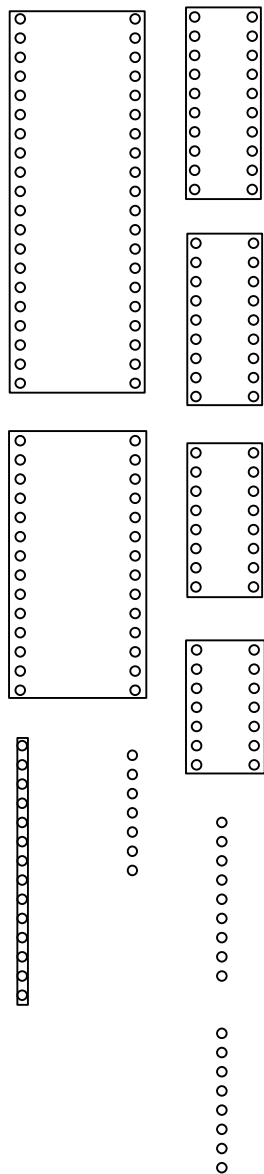


VIEW FROM BOTTOM OF BOARD
REMEMBER TO REVERSE THE ORDER OF SIGNALS SHOWN IN DATA SHEETS.
CAREFUL! IT'S EASY TO MAKE MISTAKES.



V _{CC}	20	1	DIR
/OE	19	2	A1
B1	18	3	A2
B2	17	4	A3
B3	16	5	A4
B4	15	6	A5
B5	14	7	A6
B6	13	8	A7
B7	12	9	A8
B8	11	10	GND

74LS245

V _{CC}	20	1	/OC
8Q	19	2	1Q
8D	18	3	1D
7D	17	4	2D
7Q	16	5	2Q
6Q	15	6	3Q
6D	14	7	3D
5D	13	8	4D
5Q	12	9	4Q
CLK	11	10	GND

74LS374

V _{CC}	16	1	A0
SDA	15	2	A1
SCL	14	3	A2
/INT	13	4	P0
P7	12	5	P1
P6	11	6	P2
P5	10	7	P3
P4	9	8	GND

PCF8574

1	V _{SS}	LCD
2	V _{CC}	
3	V _{EE}	
4	RS	
5	R/W	
6	E	
7	DB0	
8	DB1	
9	DB2	
10	DB3	
11	DB4	
12	DB5	
13	DB6	
14	DB7	

V _{CC}	16	1	C1+
GND	15	2	V+
T1 _{OUT}	14	3	C1-
R1 _{IN}	13	4	C2+
R1 _{OUT}	12	5	C2-
T1 _{IN}	11	6	V-
T2 _{IN}	10	7	T2 _{OUT}
R2 _{OUT}	9	8	R2 _{IN}

ST232

V _{CC}	8	1	A0
WP	7	2	A1
SCL	6	3	A2
SDA	5	4	V _{SS}

24LC16

SRAM	
28	V _{CC} A14
27	/WE A12
26	A13 A7
25	A8 A6
24	A9 A5
23	A11 A4
22	/OE A3
21	A10 A2
20	/CE A1
19	D7 A0
18	D6 D0
17	D5 D1
16	D4 D2
15	D3 GND

EPROM	
28	V _{CC} VPP
27	A14 A12
26	A13 A7
25	A8 A6
24	A9 A5
23	A11 A4
22	/OE A3
21	A10 A2
20	/CE A1
19	D7 A0
18	D6 D0
17	D5 D1
16	D4 D2
15	D3 GND

74LS373	
20	1
19	2
18	3
17	4
16	5
15	6
14	7
13	8
12	9
11	10

8051	
40	V _{CC} P1.0
39	AD0 P1.1
38	AD1 P1.2
37	AD2 P1.3
36	AD3 P1.4
35	AD4 P1.5
34	AD5 P1.6
33	AD6 P1.7
32	AD7 RST
31	/EA RXD
30	ALE TXD
29	/PSEN P3.2
28	A15 P3.3
27	A14 P3.4
26	A13 P3.5
25	A12 /WR
24	A11 /RD
23	A10 XTAL2
22	A9 XTAL1
21	A8 GND

V _{CC}	16	1	A
Y0	15	2	B
Y1	14	3	C
Y2	13	4	/G2A
Y3	12	5	/G2B
Y4	11	6	G1
Y5	10	7	Y7
Y6	9	8	GND

74LS138

V _{CC}	14	1	A1
B4	13	2	B1
A4	12	3	Y1
Y4	11	4	A2
B3	10	5	B2
A3	9	6	Y2
Y3	8	7	GND

74LS08

V _{CC}	14	1	1A
2D	13	2	1B
2C	12	3	NC
NC	11	4	1C
2B	10	5	1D
2A	9	6	1Y
2Y	8	7	GND

74LS21

V _{CC}	16	1	1C
/ZC	15	2	/1G
/ZG	14	3	B
A	13	4	1Y3
2Y3	12	5	1Y2
2Y2	11	6	1Y1
2Y1	10	7	1Y0
2Y0	9	8	GND

74LS156

V _{CC}	14	1	A1
B4	13	2	B1
A4	12	3	Y1
Y4	11	4	A2
B3	10	5	B2
A3	9	6	Y2
Y3	8	7	GND

74LS00

V _{CC}	14	1	1Y
4Y	13	2	1A
4B	12	3	1B
4A	11	4	2Y
3Y	10	5	2A
3B	9	6	2B
3A	8	7	GND

74LS02

It might be necessary to put the pin numbers inside the package outline for narrow packages, since there isn't as much space between pins for signal names.

Consider not using the outlines of the packages, and just cutting out the signal names and pin numbers and using glue to attach those to your solid white paper backing. It might give better results than trying to design the entire image in Visio and printing the whole image onto a paper to be used for backing.