What function does this circuit implement?

\[ v_o = 1/2 \ v_2 - 1/4 \ v_1 \]

A

\[ v_o = 1/4 \ v_2 - 1/2 \ v_1 \]

B

\[ v_o = -4 \ v_2 + 2 \ v_1 \]

C

\[ v_o = -4 \ v_2 - 2 \ v_1 \]

D

\[ v_o = 4 \ v_2 - 2 \ v_1 \]

E
So port two is two cascaded inverting amps with total gain \((-2)(-2)\) and port one is a single inverting amp with gain \((-2)\). The signals sum in the second stage.